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Complete if Known

Application Number	09/782743
Filing Date	February 13, 2001
First Named Inventor	Rhodes Ph.D., Howard
Group Art Unit	2823
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: 00303.592US1

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US PATENT DOCUMENTS

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
LP	US-5173438	12/22/1992	Sandhu, G.	437	63	02/13/1991
	US-5221864	06/22/1993	Galbi, D.	307	296.8	12/17/1991
	US-5491657	02/13/1996	Haddad, S. S., et al	365	185.27	02/24/1995
	US-5718800	02/17/1998	Juengling, W.	156	643.1	11/08/1995
	US-5757038	03/26/1998	Tiwari, S., et al	257	192	11/06/1995
	US-5856945	01/05/1999	Lee, P., et al	365	185.3	08/05/1997

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T ²
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OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
LP		BUDE, J., et al. , "EEPROM/Flash Sub 3.0V Drain-Source Bias Hot Carrier Writing", <u>IEDM</u> , (1995), pp. 989-991	
		KOIKE, H., et al. , "Process Simplification in Deep Submicron CMOS Fabrication", <u>IEEE - International Symposium on Semiconductor Manufacturing</u> , (1995), pp. 24-27	
		KOIKE, H., et al. , "Simple and Quick Turnaround Time Fabrication Process for Deep Submicrometer CMOS Generation", <u>IEEE Transactions on Semiconductor Manufacturing</u> , 9(4), (1996), pp. 489-494	

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Substitute Disclosure Statement Form (PTO-1449)

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